DATENBLATT

AO-1604LX-USB

HABEN SIE FRAGEN ODER
WÜNSCHEN SIE EIN INDIVIDUELLES ANGEBOT?
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Erwähnte Firmen- und Produktnamen sind zum Teil eingetragene Warenzeichen der jeweiligen Hersteller.
AO-1604LX-USB

High-Resolution Analog Output Unit for USB

This product is a USB2.0-compliant analog output unit that extends the analog output function of USB port of PCs. This product features high-precision analog outputs, digital inputs, digital outputs, and a counter function.

As there is compatible with PCI bus-compatible board DA16-4(LPCI)L and PCI Express bus-compatible board AO-1604L-LPE in terms of connector shape and pin assignments, it is easy to migrate from the existing system.

Using the bundled API function library package [API-USBP(WDM)], you can create Windows application software for this board in your favorite programming language supporting Win32 API functions, such as Visual Basic or Visual C/C++.

With plug-ins for the dedicated libraries, this product also supports MATLAB and LabVIEW.

* Specifications, color and design of the products are subject to change without notice.

Features

Multi-function
The unit contains analog outputs (16bit, 4channels), control signal(3 points) of analog output, digital inputs (LVTTL level 4channels), digital outputs (LVTTL level 4channels), and counters (32bit binary, LVTTL level 1channels). Combining all these features on one unit allows complex systems to be implemented even on PCs with USB port only.

Buffer memory
The analog outputs have their own buffer memory. You can also perform analog output in the background, independent of software and the current status of the PC.

Filter function for easy connection of external signals
The digital input signals, counter input signals, and the external control signals for analog output incorporate a digital filter to prevent wrong recognition of input signals from carrying noise or a chattering.

Compatible with PCI / PCI Express bus board in it’s design. Common connector shape and pin assignment with PCI / PCI Express bus board
This product has the common connector shape and pin assignment with PCI bus board DA16-4(LPCI)L, PCI Express bus board AO-1604L-LPE so you can use the common cables and accessories, it is easy to migrate from the existing system.

Compatible to USB1.1/USB2.0
Compatible to USB1.1/USB2.0 and capable to achieve high speed transfer at HighSpeed (480 Mbps).

Software-based calibration function
Calibration of analog output can be all performed by software. Apart from the adjustment information prepared before shipment, additional adjustment information can be stored according to the use environment.

USB HUB function, the CONTEC’s USB supported products (Max. 4) can be used.
This product has the USB HUB function. *2
Max. 4 AO-1604LX-USB can be used in 1 USB port of PC. When you use 4 or more AO-1604LX-USB, you can do by connecting AO-1604LX-USB to the another USB port of PC side.
Also, you can connect the CONTEC’s USB supported products other than AO-1604LX-USB to the USB port of AO-1604LX-USB. *3*4

Windows compatible driver libraries are attached.
Also, driver library API-USBP(WDM) that makes it possible to create applications of Windows is attached.

Plug-ins for the dedicated libraries, this product also supports MATLAB and LabVIEW.
We offer a dedicated library [ML-DAQ], which allows you to use this product on MATLAB by The MathWorks as well as another dedicated library [VI-DAQ], which allows you to use the product on LabVIEW by National Instruments. These dedicated libraries are available, free of charge (downloadable), on our web site.

* There are some differences of the specifications between this product and AO-1604L-LPE. DA16-4(LPCI)L, PCI Express bus board AO-1604L-LPE so you can use the common cables and accessories, it is easy to migrate from the existing system.
*1 This product cannot be stacked up for installation.
*2 Do not connect the device other than that of CONTEC’s USB to the USB port included on the AO-1604LX-USB. Otherwise, this may cause a failure or malfunction.
*3 When connecting multiple units with USB HUB function and set up them, do one at a time and complete setup for the previous unit before starting to do the next unit.
**Specification**

<table>
<thead>
<tr>
<th>Item</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Analog output</strong></td>
<td></td>
</tr>
<tr>
<td>Isolated specification</td>
<td>Non-isolated</td>
</tr>
<tr>
<td>Number of output channels</td>
<td>4 channels</td>
</tr>
<tr>
<td>Output range</td>
<td>12 bit or 10V</td>
</tr>
<tr>
<td>Absolute max. output current</td>
<td>3mA</td>
</tr>
<tr>
<td>Output impedance</td>
<td>15 ohm or less</td>
</tr>
<tr>
<td>Resolution</td>
<td>16 bit</td>
</tr>
<tr>
<td>Non-Linearity error</td>
<td>±5LSB</td>
</tr>
<tr>
<td>Conversion speed</td>
<td>10ns/sec</td>
</tr>
<tr>
<td>Buffer memory</td>
<td>16k Word</td>
</tr>
<tr>
<td>Conversion start trigger</td>
<td>Software/external trigger</td>
</tr>
<tr>
<td>Conversion stop trigger</td>
<td>Number of sampling times/external trigger/software</td>
</tr>
<tr>
<td>External start signal</td>
<td>LV TTL level (Rising or falling edge can be selected by software) Digital filter (1 µsec can be selected by software)</td>
</tr>
<tr>
<td>External stop signal</td>
<td>LV TTL level (Rising or falling edge can be selected by software) Digital filter (1 µsec can be selected by software)</td>
</tr>
<tr>
<td>External clock signal</td>
<td>LV TTL level (Rising or falling edge can be selected by software) Digital filter (1 µsec can be selected by software)</td>
</tr>
<tr>
<td>Digital I/O</td>
<td></td>
</tr>
<tr>
<td>Number of input channels</td>
<td>Non-isolated input 8 channels (LV TTL level positive logic)</td>
</tr>
<tr>
<td>Number of output channels</td>
<td>Non-isolated output 8 channels (LV TTL level positive logic)</td>
</tr>
<tr>
<td><strong>Counter</strong></td>
<td></td>
</tr>
<tr>
<td>Number of channels</td>
<td>1 channel</td>
</tr>
<tr>
<td>Counting system</td>
<td>Up count</td>
</tr>
<tr>
<td>Max. count</td>
<td>FFFFFFFFH (Binary data, 32bit)</td>
</tr>
<tr>
<td>Number of external inputs</td>
<td>LV TTL level - 2 (Gate/Up), 1ch, Gate (High Level), Up (Rising edge)</td>
</tr>
<tr>
<td>Number of external outputs</td>
<td>LV TTL level - 1 ch, Count match output (positive logic, pulse output)</td>
</tr>
<tr>
<td>Frequency response</td>
<td>10MHz (Max.)</td>
</tr>
<tr>
<td><strong>USB</strong></td>
<td></td>
</tr>
<tr>
<td>Bus specification</td>
<td>USB Specification 2.0/1.1 standard</td>
</tr>
<tr>
<td>USB transfer rate</td>
<td>12Mbps (Full-speed), 480Mbps (High-speed) *2</td>
</tr>
<tr>
<td>Power supply</td>
<td>Self power *3</td>
</tr>
<tr>
<td>Attached AC adaptor</td>
<td>POA200-20-2</td>
</tr>
<tr>
<td>USB I/O Unit (POA200-20-2)</td>
<td>90 - 264VAC 5.0VDC5%/2.0A (Max.)</td>
</tr>
<tr>
<td>Cable length</td>
<td>UV Cable : about 1.5m, AC Cable length : about 1.5m</td>
</tr>
<tr>
<td><strong>Common section</strong></td>
<td></td>
</tr>
<tr>
<td>Number of terminals used</td>
<td>127 terminals (Max.) *4</td>
</tr>
<tr>
<td>Power consumption (Max.)</td>
<td>5VDC 700mA</td>
</tr>
<tr>
<td>Operating condition*5</td>
<td>0 - 50ºC, 10 - 95%RH (No condensation)</td>
</tr>
<tr>
<td>Physical dimensions (mm)</td>
<td>180L x 140D x 34H (No protrusions)</td>
</tr>
<tr>
<td>Weight</td>
<td>400g (Not including the USB cable, attachment)</td>
</tr>
<tr>
<td>Connector</td>
<td>PCB50PS-0.5P, POA-AC0205-0.5A, or equivalent to it</td>
</tr>
<tr>
<td>Attached cable length</td>
<td>USB Cable : 1.8m</td>
</tr>
<tr>
<td>Certification</td>
<td>RoHS, FFC, VCCI</td>
</tr>
</tbody>
</table>

*1: A linearity error approximately 0.1% of full-range may occur when operated at 0ºC or 50ºC ambient temperature.
*2: The USB transfer speed depends on the host PC environment used (OS and USB host controller).
*3: The supplied current is insufficient in the bus power. Please use the attached AC adaptor (POA200-20-2).
*4: As a USB hub is also counted as one device, you cannot just connect 127 USB terminals.
*5: To suppress the heating, ensure that there are spaces for ventilation (about 5cm) around this product.

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**Support Software**

Windows version of analog I/O driver API-AIO(WDM) [Stored on the bundled CD-ROM driver library API-USB(WDM)]

The API-AIO(WDM) is the Windows version driver library software that provides products in the form of Win32 API functions (DLL). Various sample programs such as Visual Basic and Visual C++, etc and diagnostic program useful for checking operation is provided.

< Operating environment >

- **Adaptation language**: Visual Basic, Visual C++, Visual C#, Delphi, C++ Builder

For more details on the supported OS, applicable language and how to download the updated version, please visit the CONTEC’s Web site (http://www.contec.com/apiusbpl/).

Data Acquisition library for MATLAB ML-DAQ

(Available for downloading (free of charge) from the CONTEC web site.)

This is the library software which allows you to use our analog I/O device products on MATLAB by the MathWorks. Each function is offered in accordance with the interface which is integrated in MATLAB’s Data Acquisition Toolbox. See http://www.contec.com/mldaq/ for details and download of ML-DAQ.

Data acquisition VI library for LabVIEW VI-DAQ

(Available for downloading (free of charge) from the CONTEC web site.)

This is a VI library to use in National Instruments LabVIEW. VI-DAQ is created with a function form similar to that of LabVIEW’s Data Acquisition VI, allowing you to use various devices without complicated settings. See http://www.contec.com/vidaq/ for details and download of VI-DAQ.

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**Cable & Connector**

**Shield Cable with 50-Pin Mini-Ribbon Connectors at either Ends**

- **Shield Cable with 50-Pin Mini-Ribbon Connector at one End**

**Accessories**

**Screw Terminal Unit (M3 x 50P)**

- EPD-50A *1*2

**BNC Terminal Unit (for analog input 8ch)**

- ATP-8L *1*3

**USB I/O Unit Bracket for X Series**

- BRK-USBX

**AC adapter** (input: 90 - 264VAC, output: 5VDC 2.0A)

- POA200-20-2 *4

**AC-DC power supply unit** (input: 85 - 132VAC, output: 5VDC 3.0A)

- POA-AC13GY

**DC-DC power supply unit** (input: 10 - 30VDC, output: 5VDC 3.0A)

- POA-DD10GY

**DC-DC power supply unit** (input: 30 - 50VDC, output: 5VDC 3.0A)

- POA-DD43GY
Packing List

- Unit [AO-1604LX-USB] …1
- AC adaptor …1
- AC Cable (for 125VAC) …1
- USB cable (1.8m) …1
- USB cable attachment on the main unit’s side (For Mini B connector side) …1
- Clamps for prevention of cable on the main unit’s side …1
- CD-ROM *1 [API-USBP(WDM)] …1
- First step guide … 1
- Power connector MC1,5/3-ST-3,5 …1
- Ferrite core …1

*1 The CD-ROM contains the driver software and User’s Guide (this guide)
How to Connect the Connectors

Connector Shape
To connect an external device to this product, plug the cable from the device into the interface connector (CN1) of unit shown below.

* Please refer to page 3 for more information on the supported cable and accessories.

Connector Pin Assignment

Pin Assignments of Interface Connector(CN1)

<table>
<thead>
<tr>
<th>Pin Number</th>
<th>Pin Assignment</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1-24</td>
<td>Digital Output Connection</td>
<td>Digital output signals, Counter signals and Control signals Connection</td>
</tr>
<tr>
<td>25-36</td>
<td>Analog Output Connection</td>
<td>Analog output signal. The numbers correspond to channel numbers.</td>
</tr>
<tr>
<td>37-48</td>
<td>Analog Ground</td>
<td>Common analog ground for analog output signals.</td>
</tr>
<tr>
<td>49-59</td>
<td>AO External Stop Trigger</td>
<td>External stop trigger for analog output.</td>
</tr>
<tr>
<td>60-71</td>
<td>AO External Sampling Clock</td>
<td>External sampling clock input for analog output.</td>
</tr>
<tr>
<td>72-83</td>
<td>Digital Input 00 - Digital Input 03</td>
<td>Digital input signals.</td>
</tr>
<tr>
<td>84-95</td>
<td>Digital Output 00 - Digital Output 03</td>
<td>Digital output signals.</td>
</tr>
<tr>
<td>96-107</td>
<td>Counter Gate Control Input</td>
<td>Gate control input signal for counter.</td>
</tr>
<tr>
<td>108-119</td>
<td>Counter Up Clock Input</td>
<td>Count-up clock input signal for counter.</td>
</tr>
<tr>
<td>120-131</td>
<td>Counter Output</td>
<td>Counter output signal.</td>
</tr>
<tr>
<td>132-143</td>
<td>Digital Ground</td>
<td>Common digital ground for digital I/O signals, external trigger inputs,</td>
</tr>
<tr>
<td></td>
<td></td>
<td>external sampling clock inputs, and counter I/O signals.</td>
</tr>
<tr>
<td>144-155</td>
<td>Reserved</td>
<td>Reserved pin.</td>
</tr>
<tr>
<td>156-157</td>
<td>N.C.</td>
<td>No connection to this pin.</td>
</tr>
</tbody>
</table>

CAUTION
- Do not connect any of the outputs and power outputs to the analog or digital ground. Neither connect outputs to each other. Doing either can result in a fault.
- If analog and digital ground are shorted together, noise on the digital signals may affect the analog signals. Accordingly, analog and digital ground should be separated.
- Leave "Reserved" pins unconnected. Connecting these pins may cause a fault in the board.

Analog Output Signal Connection

Analog Output Connection (Flat Cable)
This section shows how to connect the analog output signal by using a flat cable or a shielded cable. The following figure shows an example of flat cable connection. Connect the signal source and ground to the CN1 analog output.

Analog Output Connection (Shielded Cable)
The following figure shows an example of shield cable connection. Use shield cable if the distance between the signal source and this product is long or if you want to provide better protection from noise. For the CN1 analog output, connect the core wire to the signal line and connect the shielding to ground.

Digital I/O signals, Counter signals and Control signals Connection
The following sections show examples of how to connect digital I/O signals, counter I/O signals, and other control I/O signals (external trigger input signals, sampling clock input signals, etc.).

All the digital I/O signals and control signals are LVTTTL level signals.

Digital Input Connection

Digital Output Connection
About the counter input control signal

Counter Gate Control Input (refer to the chapter 3 Connector Pin Assignment) acts as an input that validate or invalidate the input of an external clock for the counter. This function enables the control of an external clock input for the counter. The external clock for the counter is effective when input is "High", and invalid when input is "Low". If unconnected, it is a pull-up in this product and remains "High". Therefore the external clock for the counter is effective when the counter gate control input is not connected.

⚠️ CAUTION

- Do not short the output signals to analog ground, digital ground, and/or power line. Doing so may damage the product.
- If connected to each output, a pull-up resistor must be about 10kΩ to pull up with a 3.3V power source.
- Each input accepts 5V TTL signals.

**Connection with 5VDC Power Supply for Self-power**

**+5 VDC Input Terminal Pinouts**

This product must be connected with 5VDC power supply (in a self-powered state).

Connect with 5VDC power supply by using +5VDC input pin.

```
5VDC

FG  V+  V-+

V+  Power supply (5V)
V-  Power supply (GND)
FG  Frame ground
```

**Connecting the AC Adapter POA200-20-2**

When using the attached AC adaptor [POA200-20-2], please connect directly to the input terminals. When the accompanying power connector (MC1,5/3-ST-3,5, suitable cable: AWG28 - 16) is used to supply power to this unit, strip the end of the suitable cable and insert it to the power connector before firmly securing it using a screw.

⚠️ CAUTION

- Connect 5VDC power supply to the main unit. Next, connect the USB cable to the PC. Do not turn it on or off when using. If you remove, USB cable is first and then 5VDC power supply.
- When the USB module is not used, leave the AC adaptor unplugged.
- Continuously using the AC adaptor heated affects its life.
- Use the AC adaptor not in a closed place but in a well-ventilated place not to be heated.
- Do not remove the power connector [MC1,5/3-ST-3,5] attached to the AC adapter.