## Digital I/O Board with Opto-Isolation for PCI Express

#### DIO-6464L-PE



\* Specifications, color and design of the products are subject to change without notice.

#### **Features**

# Optocoupler isolated inputs (compatible with current sink output), and Optocoupler isolated open-collector outputs (current sink type)

This product has the 64 channels of Optocoupler isolated input (compatible with current sink output) and 64 channels Optocoupler isolated open-collector output (current sink type) whose response speed is 200µsec. Common terminal provided per 16 channels, capable of supporting a different external power supply. Supporting driver voltages of 12 - 24 VDC for I/O.

#### Optocoupler bus isolation

As the PC is isolated from the input and output interfaces by Optocoupler, this product has excellent noise performance.

You can use 16 input signals as interrupt request signals. You can use 16 input signals as interrupt request signals and also disable or enable the interrupt in bit units and select the edge of the input signals, at which to generate an interrupt.

#### Windows/Linux drivers are available

By using the digital I/O driver, each Windows/Linux application can be created. In addition, a diagnostic program by which the operations of hardware can be checked is provided.

## This product has a digital filter to prevent wrong recognition of input signals from carrying noise or a chattering

This product has a digital filter to prevent wrong recognition of input signals from carrying noise or a chattering. All input terminals can be added a digital filter, and the setting can be performed by software.

### Output circuits include zener diodes for surge voltage protection and poly-switches for overcurrent protection.

Zener diodes are connected to the output circuits to protect against surge voltages. Similarly, polyswitches are fitted to each group of 8 channels outputs for over-current protection. Output rating: max 35VDC, 100mA per pin.

### Functions and connectors are compatible with PCI compatible board PIO-64/64L(PCI)H.

The functions same with PCI compatible board PIO-64/64L(PCI)H are provided.

In addition, as there is compatibility in terms of connector shape and pin assignments, it is easy to migrate from the existing system.

### LabVIEW is supported by a plug-in of dedicated library VI-DAQ.

Using the dedicated library VI-DAQ makes it possible to make a LabVIEW application.

This product is a PCI Express bus-compliant interface board used to provide a digital signal I/O function on a PC. This product can input and output digital signals at 12 - 24VDC.

This product features 64 Optocoupler isolated inputs and 64 Optocoupler isolated open-collector outputs. You can use 16 input signals as interrupt inputs. In addition, the digital filter function to prevent wrong recognition of input signals is provided and output transistor protection circuit (surge voltage protection and overcurrent protection).

Windows/Linux drivers are available.

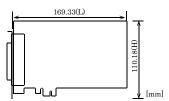
Possible to be used as a data recording device for LabVIEW, with dedicated libraries.

#### **Specification**

Item	Specification						
Input							
Input format	Optocoupler isolated input (Compatible with current sink output)(Negative logic *1)						
Number of input signal channels	64 channels (16 channels available for interrupts) (1 common per 16 channels unit)						
Input resistance	4.7kΩ						
Input ON current	2.0mA or more						
Input OFF current	0.16mA or less						
Interrupt	16 interrupt input signals are arranged into a single output of interrupt signal INTA. An interrupt is generated at the rising edge (HIGH-to-LOW transition) or falling edge (LOW-to-HIGH transition) (set by software).						
Response time	Within 200μsec						
Output							
Output format	Optocoupler isolated open-collector output (current sink type)(Negative logic *1)						
Number of output signal channels	64 channels (1 common per 16 channels unit)						
Output Output rating voltage	35VDC (Max.)						
Output current	100mA (par 1 channel) (Max.)						
Residual voltage with output on	0.5V or less (Output current≤50mA), 1.0V or less (Output current≤100mA)						
Surge protector	Zener diode RD47FM(NEC)						
Response time	Within 200μsec						
Common							
Allowable distance of signal extension	Approx. 50m (depending on wiring environment)						
I/O address	Any 32-byte boundary						
Interruption level	1 level use						
Max. board count for connection	16 boards including the master board						
Isolated power	250Vrms						
External circuit power supply	12 - 24VDC(±10%)						
Power consumption (Max.)	3.3VDC 600mA						
	0 - 50°C, 10 - 90%RH (No condensation)						
Bus specification	PCI Express Base Specification Rev. 1.0a x1						
Dimension (mm)	169.33(L) x 110.18(H)						
Connector	100 pin 0.8mm pitch connector [F (female) type] x 2 HDRA-E100W1LFDT1EC-SL+[HONDATSUSHIN KOGYO CO., LTD.] or equivalent to it						
Weight	215g						
Certification	VCCI Class A, CE Marking (EMC Directive Class A, RoHS Directive), UKCA, KC						

<sup>\*1</sup> Data "0" corresponds at the High level and data "1" correspond at the Low level.

#### **Board Dimensions**



The standard outside dimension (L) is the distance from the end of the board to the outer surface of the slot cover.



#### **Support Software**

You should use CONTEC support software according to your purpose and development environment.

The name of the documents	Contents	How to get
Digital I/O Driver software API-DIO(WDM)	Driver software of digital input and output for Windows.	Download (ZIP)
Digital I/O Driver software API-DIO(LNX)	Driver software of digital input and output for Linux.	Download (tgz)
LabVIEW-support data acquisition library DAQfast for LabVIEW	This is a data collection library to use in the LabVIEW by National Instruments. With Polymorphic VI, our design enables a LabVIEW user to operate seamlessly. Our aim is that the customers to perform easily, promptly what they wish to do.	

<sup>\*</sup> Download the software from the CONTEC website.

#### Option

Item	Model	Description				
Cable *1	PCB100PS-0.5 (0.5m) PCB100PS-1.5 (1.5m) PCB100PS-3 (3m) PCB100PS-5 (5m)	Shielded Cable With Two 100pin Connector				
	PCB100/96PS-1.5 (1.5m) PCB100/96PS-3 (3m) PCB100/96PS-5 (5m)	Connection Conversion Shield Cable (100P□96P)				
	PCA100P-1.5 (1.5m) PCA100P-3 (3m) PCA100P-5 (5m)	Flat Cable with One 100-Pin Connector				
	PCB100WS-1.5 (1.5m) PCB100WS-3 (3m) PCB100WS-5 (5m)	Connection Conversion Shield Cable (100P□□□P D-SUB x 2)				
Accessories	EPD-100A *2*4*7	Screw Terminal Unit (M3 x 100P)				
	EPD-96A *2*5*7	Screw Terminal Unit (M3 x 96P)				
	EPD-96 *2*5	Screw Terminal Unit (M3.5 x 96P)				
	DTP-64A *2*5	Terminal Unit for Cables (M3 x 96P)				
	CCB-96 *2*5	Connection Conversion Card (96-Pin → 37-Pin x 2)				
	EPD-37A *3*6*7	Screw Terminal Unit (M3 x 37P)				
	EPD-37 *3*6	Screw Terminal Unit (M3.5 x 37P)				
	DTP-3C *3*6	General Purpose Terminal (M3 x 37P)				
	DTP-4C *3*6	Screw Terminal (M2.6 x 37P)				
	CM-64L *2*5	Signal Monitor / Output Accessory for Digital I/O (64P)				

- \*1 If using both the CNA and CNB connectors, two cable sets are required.
- \*2 If using both the CNA and CNB connectors, two each of the accessories and cable sets are required.
- \*3 If using both the CNA and CNB connectors, two cable sets are required. You will also require sufficient terminal blocks for the number of I/O points you are using.
- \*4 PCB100PS optional cable is required separately.
- \*5 PCB100/96PS optional cable is required separately.
- \*6 PCB100WS optional cable is required separately.
- 7 "Spring-up" type terminal is used to prevent terminal screws from falling off.
- \* Check the CONTEC's Web site for more information on these options.

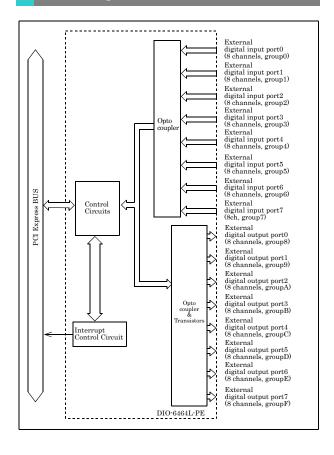
#### **Packing List**

Board [DIO-6464L-PE] ...1 First step guide ... 1 Disk \*1 [API-PAC(W32)] ...1 Serial number label...1

Product Registration & Warranty Certificate...1

\*1 The Disk contains the driver software and User's Guide.

#### **Block Diagram**

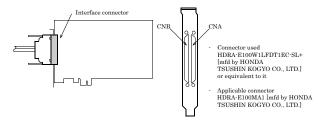




#### How to connect the connectors

#### **Connector shape**

The on-board interface connector (CNA, CNB) is used when connecting this product and the external devices.



\* Please refer to page 2 for more information on the supported cable and accessories.

#### **Connector Pin Assignment**

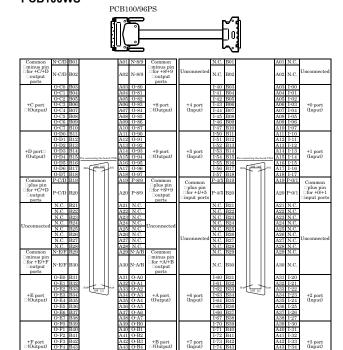
Pin Assignments Interface Connector (CNA, CNB)

Plus pin															
P-EF   90   49 P-AB   4/4-B   000   100	Common	P-E/F	100			50	P-A/B	Common		N.C.	1		51	N.C.	1
## P.EF 99	plus pin							plus pin							
## P.EF 99	for							for					ı		1
Section   Column	+E/+F	P-E/F	99			49	P-A/B	+A/+B		N.C.	2		52	N.C.	1
Section   Column	output							output							1
Common   Color   Common   Color   Co	ports														1
Components   Com		0-F7	QQ.			48	O-B7	_		N.C.	3		53	NC	
#F port									-						
## pot to   Pet   15   10   10   10   10   10   10   10															
(Output)   OF3   94   94   94   94   94   94   94   9						46	O-B5			N.C.			55		1
(Output)   OF7   94   95   95   96   96   97   97   97   97   97   97	+F port	0-F4	95			45	O-B4	+B port		N.C.	6	CNA	56	N.C.	
Common Number   Num	(Output)	O E2	0.4	CN	В	44	O B2	(Output)		* 1.00	7	CNA	67	1.40	
OFT   92	(Output)				- 1			(Output)				1 7 7			1
DFO   91   OFO   91   OFO				100	50							1 51			1
OE7   50   OE8   S0   OE8		0-F1	92	1	41	42	O-B1			* I-02	9	11 11	59	1-42	1
OE7   50   OE8   S0   OE8		O-F0	91			41	O-B0		+0 port	* 1-03	10		60	1-43	+4 port
Express   Box						40									
#E port									(IIIput)						(III)
## ports   Common   C															1
Couring   Cour		O-E5	88			38	O-A5			* I-06	13		63	1-46	1
Couring   Cour	+F port	O-F4	87			37	O-A4	+A port		* 1-07	14		64	1-47	1
DE2   85   OA2   S3   OA2   OA1   OE0   S3   OA2   OE0   OE0   S3   OA3   OE0   O															
OE1   84   OA1   OE2   83   OA1   OE2	(Output)							(Output)							1
DED   83   NAS   NEST   87   10   11   13   18   86   ISS   45 port   17   15   15   16   16   16   16   16   16	1				- 11				1			11 11			1 1
NEFF   82	1	0-E1	84		- 11	34	0-A1		1	* I-12	17	11 11	67	1-52	1 1
NEFF   82	1	O.FO	83		- 11	33	0-40		±1 port	* I-13	18	11 11	68	1-53	±6 port
Common   Number   File   State   Sta	_				- 11				(Input)			11 11			
Common   Number   Policy   P	I				- 11			l J	(mput)						(mput)
minus pin (LEF 78)   23   AAAB minus pin   11,17   22   1,27   1,	C			1 1 1	- 11	31		C	1	* I-15			70		
Number   N	Common	N-F/F	80			30	N-A/B	Common		* I-16	21		71	1-56	1
### ### ### ### ### ### ### ### ### ##								minus pin					72		1
outputs ports         N.E.F.         77         27         N.A.B. output ports         plus pin ports         Pol1 24 ports         74         P-4/5 4/-5 ports         74         P-4/5 ports         74         P-4/5 ports         74         P-4/5 ports         74         P-4/5 ports         75         N.C. 25         N.C. 26         76         N.C. 27         77         N.C. 27         N.C. 27         N.C. 27         77         N.C. 27         N.C. 28         N.C. 29         N.C. 29         N.C. 20         N.C. 20 <t< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>tor</td><td>_</td><td></td><td></td><td></td><td></td><td></td><td>_</td></t<>								tor	_						_
ports N.E.F. 77		N-E/F	78			28	N-A/B			P-0/1	23		73	P-4/5	
N-E    77								output	plus pin						plus pin
N.C.   75   28   N.C.   N.C.   25   N.C.   N.C.   25   N.C.   N.C.   25   N.C.   N.C.   25   N.C.   N.C.   26   N.C.   N.C.   27   N.C.   N.C.   27   N.C.   N.C.   28   N.C.   N.C.   29   N.C	ports	NI E/E	77			27	N A/D	ports		D 0/1	24		74	D 4/6	
N.C.   76		IN-E/F	//			21	IN-M/D			P-0/ I	24		74	P-4/3	
N.C.   76									input ports						input ports
Common   N.C.   75     25   N.C.     N.C.   29     76   N.C.     N.C.   29     N.C.     N.C.   29     N.C.     N.C.   29   N.C.     N.C.   29     N.C.     N.C.   29     N.C.     N.C.   29   N.C.     N.C.   29   N.C.   N.C.   29   N		N.C.	76			26	N.C.			N.C.	25		75	N.C.	
Common   P-CID   74   P-89   Common   P-CID   74   P-89   Common   P-CID   75   P-80															
plus pin															
Componing   P-CLD   73   22 P-8/9   9/49	Common	P-C/D	74			24	P-8/9			N.C.	27		//	N.C.	
+C/+D P-C/D 73	plus pin							plus pin							
output ports         cutput 0.058 77         cutput 22 O.37 ports         N.C. 29 N.C. 31         78 N.C. 30 N.C. 31         78 N.C. 30 N.C. 31         78 N.C. 30 N.C. 31         78 N.C. 30 N.C. 31         78 N.C. 31         78 N.C. 32         78 N.C. 3	for							for							1
Description	+C/+D	P-C/D	73			23	P-8/9	+8/+9		N.C.	28		78	N.C.	1
Description	output							output							1
ODT   72	norts							ports							1
Output   O	F-1.00	O D7	72			22	0.07	P=1.45		N.C	20		70	NC	
**D of Code   60   70   70   70   70   70   70   70															
**Optrof** OD41 69 (Output) OD31 68															
(Output) OD3 88 (1) 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		O-D5	70			20	0-95			N.C.	31		81	N.C.	
(Output) OD3 88 (1) 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	+D port	O-D4	69			19	O-94	+9 port		N.C.	32		82	N C	
Common   C						10		(Output)							
O-D1   68   68   69   69   69   69   69   69	(Output)							(Output)							1
0-00   65   65   65   65   65   65   65	1			1 1 1	- 11				1						ı I
OC7   641   OC8	1	O-D1	66		- 11	16	0-91		1	1-22	35	11 11	85	1-62	1 1
OC7   641   OC8	1	0.00	65		- 11	15	0.90		±2 Port	1-23	36	11 11	86	1-63	+6 port
OC6   63   02   13   O-86   12   O-85   0-85	_				- 11			-							
**C port	1				- 11				(mput)			11 11			(iriput)
+C port	1	O-C6	63		- 11	13	O-86		1	I-25	38	11 11	88	I-65	1 1
+C port	I	O-C5	62		- 11	12	0-85	l J	1	1-26	39		89	1-66	1 1
(Ouiput) OC3 60 OC1 55 OC2 55 OC3 57	+C nort			1 1 1	- 11			±9 port	1						
OC2   59   51   9   O-52	(Output)			1 1 1	- 11			(Output)	$\vdash$						-
O-C1 58 8 0-81	(Output)			l II	41			(Output)	1						
O-C1 58 8 0-81	1	O-C2	59	51	-	9	0-82	l J	1	I-31	42	50	92	I-71	
O_CO  57   7 O_SO	ı			_	J .				1			1 100			ı I
N-CO  56   6   N-89    (Input)   1-34   45   95   1-74   (Input)	1											_			
Common   N-C/O   55   5   N-899   Common   N-C/O   55   1   1   N-899   Common   N-C/O   55   1   N-899   Common   N-C/O   55   2   N-899   Common   N-C/O   55   1   N-899   Common   N-C/O   55   N-699									+3 Port						
minus pin N-CCD 54 4 N-899 minus pin N-CCD 53 3 N-899 for 137 N-CCD 53 2 N-899 for 04put ports N-CCD 51 1 N-899 po	1		56			6			(Input)	1-34			95	1-74	(Input)
minus pin N-CCD 54 4 N-899 minus pin N-CCD 53 3 N-899 for 137 N-CCD 53 2 N-899 for 04put ports N-CCD 51 1 N-899 po	Common	N-C/D	55			5	N-8/9	Common	1	1-35	46		96	1-75	1 1
N-C/D   53   3 N-89   for   1-37   48   98   1-77   1-4									1						1 1
+C/+D 52 2 N-89 +6/+9 common P-23 49 pois pin for:-2/+3 P-23 50 100 P-87 for:-6/+7 pois pin for:-2/+3 P-23 50 100 P-87 for:-6/+7 pois pin for:-2/+3 P-23 50 100 P-87 for:-6/+7 pois pin	minus pin							minus pin	1						1 1
output ports         N-C/D         51         1         N-8/9 ports         output plus pin for 2-2/3 p2/3 50 light plus pin pin ports         99 Pear Or Or Pear Pear Or Pear Or Pear Pear Pear Pear Or Pear Pear Pear Pear Pear Pear Pear Pea															
output ports         N-C/D         51         1         N-8/9         ports         Input ports         P-2/3         50         100         P-6/7         for⊆+6/+7 input ports		N-C/D	52			2	N-8/9		Common	P-2/3	49		99	P-6/7	Common
input ports input ports									plus pin						plus pin
input ports input ports	ports	N-C/D	51			1	N-8/9	ports	for = +2/+3	P-2/3	50		100	P-6/7	for = +6/+7
	1		٠.			1 '				. 2.3	50		.00		
	* 1.00	1.47							p. posto				•	_	

I-00 - I-17 can be used as interrupt signal.

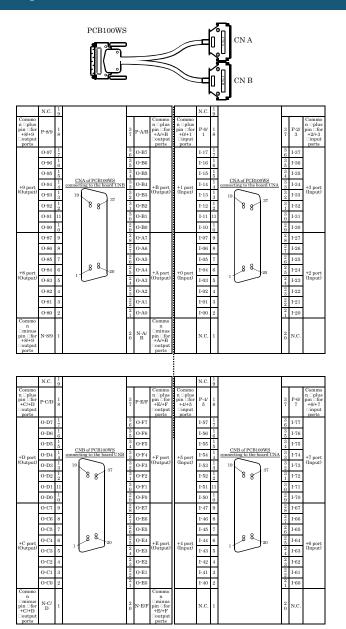
I-00 - I-77	64 channels input signal. Connect output signals from the external device to these pins.
O-80 - O-F7	64 channels output signal. Connect input signals from the external device to these pins.
P-0/1 - P-6/7	Connect the positive side of the external power supply. These pins are common to 16 channels input signal.
P-8/9 - P-E/F	Connect the positive side of the external power supply. These pins are common to 16 channels output signal.
N-8/9 - N-E/F	Connect the negative side of the external power supply. These pins are common to 16 channels output signal. One pin permissible current of the connector is 0.3A. Please connect necessary number of pins for the corresponding total current of the output 16 channels. When 16 channels are used by the output full ratings (100mA per 1 channel), it is necessary to connect six all.
N.C.	This pin is left unconnected.

### Pin assignments for connecting to the PCB100/96PS or PCB100WS



\*[] shows pin numbers specified by HONDATSUSHIN KOGYO CO., LTD.



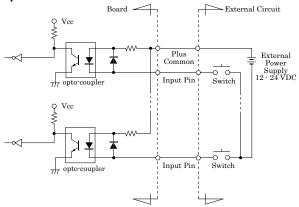


### **Connecting Input signals**

Connect the input signals to a device which can be current-driven, such as a switch or transistor output device. The connection requires an external power supply to feed currents.

The board inputs the ON/OFF state of the current-driven device as a digital value.

#### **Input Circuit**

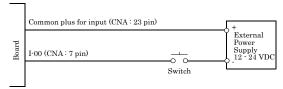


\* I-xx shows input pins.

This product input circuit of interface is illustrated in the image above.

The signal inputs are isolated by Optocoupler (ready to accept current sinking output signals). The board therefore requires an external power supply to drive the inputs. The power requirement for each input pin is about 5.1mA at 24VDC (about 2.6mA at 12VDC).

#### Connecting to the Switch



When the switch is ON, the corresponding bit contains 1. When the switch is OFF, by contrast, the bit contains 0.



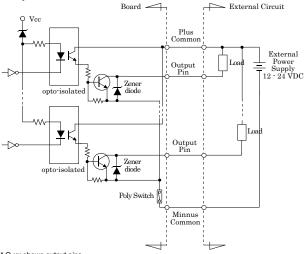
#### **Connecting Output signals**

Connect the output signals to a current-driven controlled device such as a relay or LED.

The connection requires an external power supply to feed currents.

The board controls turning on/off the current-driven controlled device using a digital value.

#### **Output Circuit**



\* O-xx shows output pins

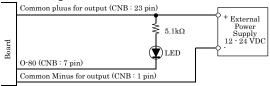
This product output circuit of interface is illustrated in the image above.

The signal output section is an Optocoupler isolated, open-collector output (current sink type). Driving the output section requires an external power supply.

The rated output current per channel is 100mA at maximum. The output section can also be connected to a TTL level input as it uses a low-saturated transistor for output. The residual voltage (low-level voltage) between the collector and emitter with the output on is 0.5V or less at an output current within 50mA or at most 1.0V at an output current within 100mA. A zener diode is connected to the output transistor for protection from surge voltages. A PolySwitch-based overcurrent protector is provided for every eight output transistors. When the overcurrent protector works, the output section of the board is temporarily disabled. If this is the case, turn of the power to the PC and the external power supply and wait for a few minutes, then turn them on back.

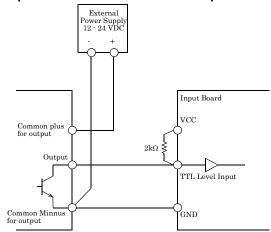
When the PC is turned on, all output are reset to OFF.

#### Connecting to the LED



When "1" is output to a relevant bit, the corresponding LED comes on When "0" is output to the bit, in contrast, the LED goes out.

#### **Example of Connection to TTL Level Input**



## **Connecting the Sink Type Output and Sink Output Support Input**

The following example shows a connection between a sink type output (output board) and a sink output support input (input board). Refer to this connection example when you connect such boards to each other.

